

# 25th IFIP/IEEE International Conference on Very Large Scale Integration

VLSI-SoC 2017

23-25 October 2017

Abu Dhabi, United Arab Emirates Yas Viceroy Hotel, Yas Island, Abu Dhabi

# The PhD Forum

VLSI-SoC 2017's Ph.D. Forum is a poster session aimed at the exchange of ideas and experiences of Ph.D. students from different parts of the world. Elected Ph.D. students have an opportunity to discuss their thesis and research work with specialists within the system and design automation communities. This offers a good opportunity for students to receive valuable feedback and gain exposure in the job market. Furthermore, this forum also provides a great chance for industry officials to meet junior researchers, giving an avenue for incorporating the latest research developments into their companies.

### Topics of interest include but are not limited to:

- 1. Analog, mixed-signal and sensor architectures
- 2. Digital architectures: NoC, multi-core, and reconfigurable
- 3. CAD: Synthesis and analysis
- 4. Prototyping, verification, modeling, and simulation
- 5. Circuits and systems for signal processing and communications
- 6. Low-power and thermal-aware IC design
- 7. Embedded systems: Architectures, design, and software
- 8. Emerging semiconductor technologies
- 9. Variability, reliability, and test
- 10. Hardware security

### **Fligibility**

The author must have completed at least one year of a Ph.D. program.

### Presentation

Posters will be introduced in the Ph.D. Forum Session (two-minute time slot, one slide) and next presented in a full one-hour Poster Session.

### **Publication**

Accepted posters will be given two pages in the soft proceedings. Camera-ready abstracts must follow the same guidelines as for camera-ready papers submitted to the main conference.

### Best PhD Forum Poster Award

The PhD forum selection committee will review all presented posters and recommend one poster for the Best PhD Forum Poster Award. We will have awards for second and third places as well.

### **Grants for PhD Students**

VLSI-SoC 2017 finances a limited number of travel grants for Ph.D. students, through the Technical Committee TC-10 (Computer Systems Technology) and WG 10.5 of the International Federation for Information Processing - IFIP. In principle, grants are restricted to support students who cannot be fully supported by their institutions. This rule gives preference to students being enrolled in Africa, Latin America, Eastern Europe and Asia (except Israel, Japan, Singapore, South Korea, Taiwan and China).

# How to Apply?

Send submissions to soha@cs.tufts.edu and leila@uaeu.ac.ae by July 4, 2017. Paper format should be compliant with the guidelines for regular papers.

### **Key Dates**

PhD Forum Submission: <del>July 4</del> July 21, 2017 Notification of Acceptance: <del>July 18</del> August 4, 2017 Camera-ready: <del>July 31</del> August 18, 2017 For more information, please contact: Soha Hassoun: soha@cs.tufts.edu Leila Ismail: leila@uaeu.ac.ae

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